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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10019700	01/02/2002	438		2812	

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****CONTINUING DATA VERIFIED:** JA
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**** FOREIGN APPLICATIONS VERIFIED:** A
JAPAN 11/039053 07/02/1999
JAPAN 11/039054 07/02/1999
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PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials		ATTORNEY DOCKET NO 2001-1876A

TITLE : Electric charge generating semiconductor substrate bump forming device, method of removing electric charge from electric charge generating semiconductor substrate device for removing

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg. Print Fig.
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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